illing Date: 09-Jan-2004	Electronic Patent Application Fee Transmittal							
Integrated chip package structure using silicon substrate and method of manufacturing the same inst Named Inventor/Applicant Name: Mou-Shiung Lin Dennis Alan Duchene/Shirt.ynn Mata ttorney Docket Number: led as Large Entity Itility under 35 USC 111(a) Filing Fees Description Fee Code Quantity Amount Sub-Total in USD(\$) asic Filing: ages: laims: liscellaneous-Filing: etition: atent-Appeals-and-Interference: ost-Allowance-and-Post-Issuance:	Application Number:	10755042						
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Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Submission-Information Disclosure Stmt	1806	1	180	180
	Total in USD (\$)			180